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Application Number		10602292
Filing Date		2003-06-24
First Named Inventor	Micha	el B. Doerr
Art Unit		2181
Examiner Name	Meon	ske, Tonia L.
Attorney Docket Numb	er	5860-00101

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	2	Waingold et al., "Barling it All to Software: Raw Machines," IEEE Computer, September 1997, 8 pages.									
	3 Taylor et al., "The Raw Microprocessor: A Computational Fabric for Software Circuits and General-Purpose Programs," IEEE Micro, March-April 2002, 11 pages.										
	4	Lee et al., "Space-Time Scheduling of Instruction-Level Paralleleium on a Raw Machine," Proceedings of the Eighth International Conference on Architectural Support for Programming Language and Operating Systems (ASPLOS-8),									

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5	Barua et al., "Compiler Support for Scalable and Efficient Memory Systems," IEEE Transactions on Computers, November 2001, 32 pages.	
6	Lee et al., "Convergent Scheduling," Proceedings of the 35th International Symposium on Microarchitecture (MICRO-35), November 2002,12 pages.	
7	Babb et at., "Paralleizing Applications into Silcon," Proceedings of the IEEE Workshop on FPGAs for Custom Computing Machines '99 (FCCM '99), April 1999, 11 pages.	
8	Babb et al., "The RAW Benchmark Suite: Computation Structures for General Purpose Computing," IEEE Symposium on Field-Programmable Custom Computing Machines (FCCM '97), April 1997, 10 pages.	
9	Babb et al., "Solving Graph Problems with Dynamic Computation Shructures," SPIE Photonics East: Reconfigurable Technology for Rapid Product Development & Computing, November 1995, 12 pages.	
10	Frank et al., "SUDS: Primitive Mechanisms for Memory Dependence Speculation," MIT/LCS Technical Memo LCS- TM-591, January 6, 1999, 9 pages.	
11	Barus, "Maps: A Compiler-Managed Memory System for Software-Exposed Architectures," PhD Thesis, MIT Laboratory for Computer Science, January 2000, 161 pages.	
12	Barua et al., "Maps: A Compiler-Managed Memory System for Raw Machines," Proceedings of the Twenty-Sixth International Symposium on Computer Architecture (ISCA-26), June, 1999, 12 pages.	
13	Barua et al., "Memory Bank Disambiguation using Modulo Unrolling for Raw Machines," Proceedings of the Fifth International Conference on High Performance Computing, December, 1998, 9 pages	
14	Mortz, et al., "Hot Pages. Software Caching for Raw Microprocessors," MIT/LCS Technical Memo LCS-TM-599, August, 1999, 12 pages.	
15	Miller, "Software Based Instruction Caching for the RAW Architecture," Master's Thesis, Massachusetts Institute of Technology, May, 1999, 39 pages.	

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	17	Taylor et at, "Scalar Operand Networks: On-chip Interconnect for ILP in Partitioned Architectures," MITILCS Technical Report LCS-TR-859, July 2002, 20 pages.						
	18	Taylor, "Design Decisions in the Implementation of a Raw Architecture Workstation," Master's Thesis, Massachusetts Institute of Technology, September, 1999, 90 pages.						
	19	Moritz et al., "Exploring Oplinal Cost-Performance Designs for Raw Microprocessors," Proceedings of the International IEEE Symposium on Field-Programmable Custom Computing Machines (FCCM 98), April 1998, 16 pages.						
	20	Aganval et al., "The Raw Compiler Project," Proceedings of the Second SUIF Compiler Workshop, Stanford, CA, 1997, 12 pages.						
	21	PARKER, "A component-based architecture for parallel multi-physics PDE simulation," Future Generations Computer Systems, Elsevier Science Publishers, volume 22, no. 1-2, 2006, pages 204-216.						
	22	AGARWAL, ANANT, ET AL., "The MIT Aleville Machine", Proceedings of the IEEE, Vol. 87, No. 3, March 1999, pages 430-444.						
	23	HOUZET, D. ET AL., "A shared memory model on a cluster of PCs", Microprocessors and Microsystems, IPC Business Press Lid. London, volume 23, no. 3, October 1, 1999, pages 125-134						
	24	KIMELMAN, D. ET AL., "Visualking the Execution of High Performance Fortran (HPF) Programs", Proceedings of the 9th International Parallel Processing Symposium, Santa Barbara, CA, April 25-28, 1995, IEEE Computer Society, Los Alamitos, CA, April 25, 1995, pages 750-759.						
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